

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE : NATAN VISHLITZKY, DOUGLAS E. LECRONE, IZHAR
SHARON, DANIEL A. MURPHY, WILLIAM R.
FAIRCHILD, HANA MORESHET, MARTIN FARLEY AND
ELIZABETH C. PATAPOUTIAN

SERIAL NO. : 10/688,180

FILED : October 17, 2003

FOR : METHOD FOR ENABLING OVERLAPPED INPUT/OUTPUT
REQUESTS TO A LOGICAL DEVICE USING ASSIGNED
AND PARALLEL ACCESS UNIT CONTROL BLOCKS

EXAMINER : Joshua D. Schneider

ART UNIT : 2182

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REJECTION OVER A PRIOR PATENT

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